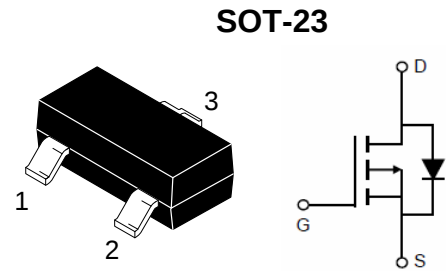


-20V/-3.0A P-Channel Advanced Power MOSFET

BVDSS	-20	V
ID	-3.0	A
RDSON@VGS=-4.5V	49.0	mΩ
RDSON@VGS=-3.3V	52.0	mΩ
RDSON@VGS=-2.5V	60.0	mΩ


Features

- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

Applications

- PWM Applications
- Load Switch
- Power Management

Order Information

Product	Package	Marking	Packing
AM20DP030T	SOT23	A1SHB	3000PCS/Reel

Absolute Maximum Ratings

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		-20	V
V_{GSS}	Gate-Source Voltage		±12	V
I_D	Continuous Drain Current	$T_A = 25^\circ\text{C}$	-3.0	A
		$T_A = 70^\circ\text{C}$	-2.5	
I_{DM}	Pulsed Drain Current ^{note1}		-12	A
P_D	Power Dissipation	$T_A = 25^\circ\text{C}$	1.2	W
		$T_A = 70^\circ\text{C}$	0.9	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		100	°C/W
T_J, T_{STG}	Operating and Storage Temperature Range		-50 to +150	°C

Electrical Characteristics ($T_C=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = -250μA	-20	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -20V, V _{GS} = 0V,	-	-	-1	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±10V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-0.4	-0.6	-1.0	V
R _{DS(on)}	Static Drain-Source on-Resistance note2	V _{GS} =-4.5V, I _D =-3.0A	-	49	60	mΩ
		V _{GS} =-3.3V, I _D =-2.0A	-	52	70	
		V _{GS} =-2.8V, I _D =-1.5A	-	56	80	
		V _{GS} =-2.5V, I _D =-1.0A	-	60	90	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz	-	330	-	pF
C _{oss}	Output Capacitance		-	50	-	pF
C _{rss}	Reverse Transfer Capacitance		-	45	-	pF
Q _g	Total Gate Charge	V _{DS} = -10V, I _D = -3.0A, V _{GS} = -4.5V	-	6.6	-	nC
Q _{gs}	Gate-Source Charge		-	0.8	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	1.4	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} = -10V,	-	11	-	ns
t _r	Turn-on Rise Time	I _D =-3A,	-	12	-	ns
t _{d(off)}	Turn-off Delay Time	R _{GEN} =3.3Ω,	-	18	-	ns
t _f	Turn-off Fall Time	V _{GS} =-4.5V,	-	30	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _s	Maximum Continuous Drain to Source Diode Forward Current		-	-	-2.5	A
I _{SD}	Source Drain Current (Body Diode)		-	-	-1.5	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _s = -2.0A	-	-0.85	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

Typical Characteristics

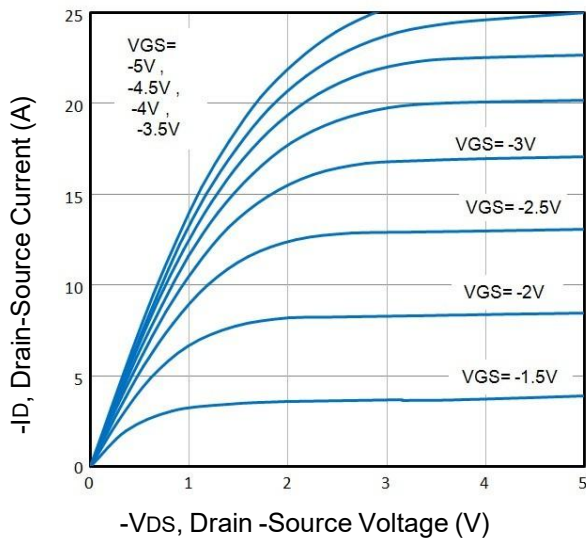


Fig1. Typical Output Characteristics

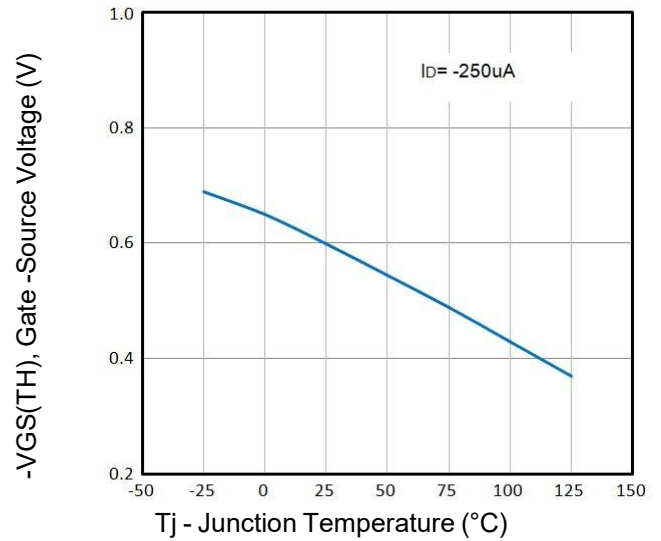


Fig2. Normalized Threshold Voltage Vs. Temperature

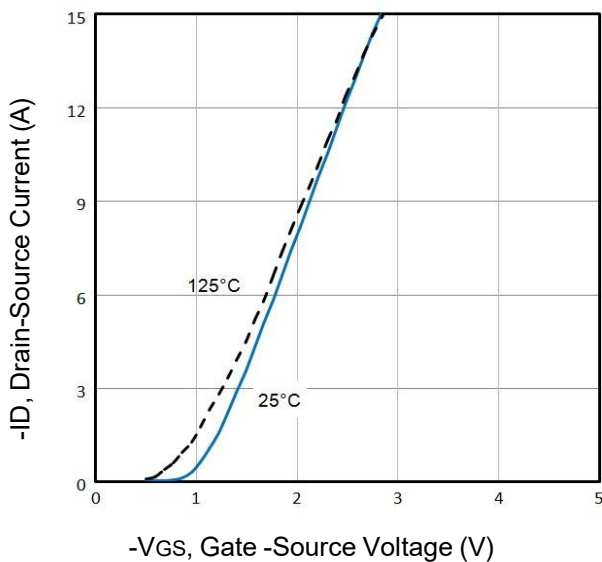


Fig3. Typical Transfer Characteristics

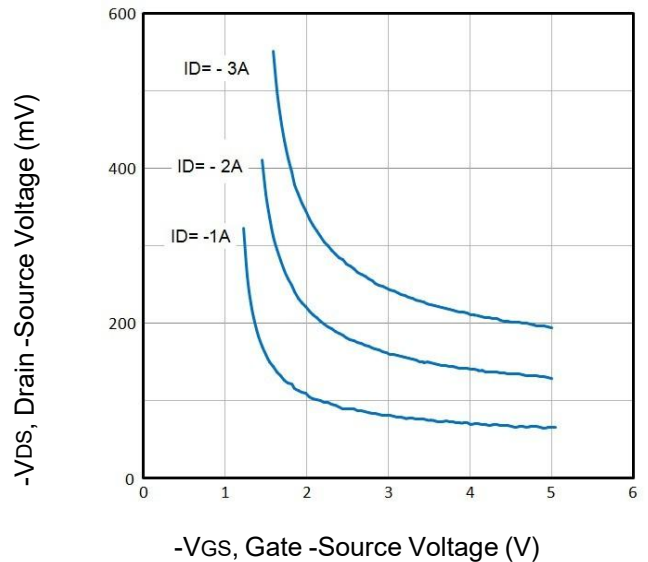


Fig4. Drain-Source Voltage vs Gate-Source Voltage

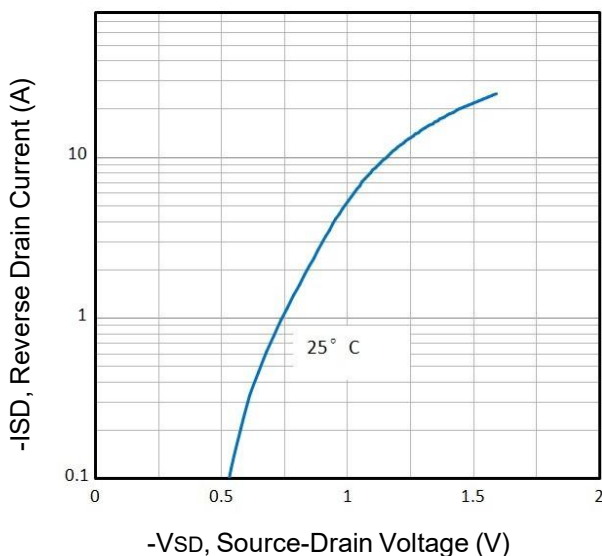


Fig5. Typical Source-Drain Diode Forward Voltage

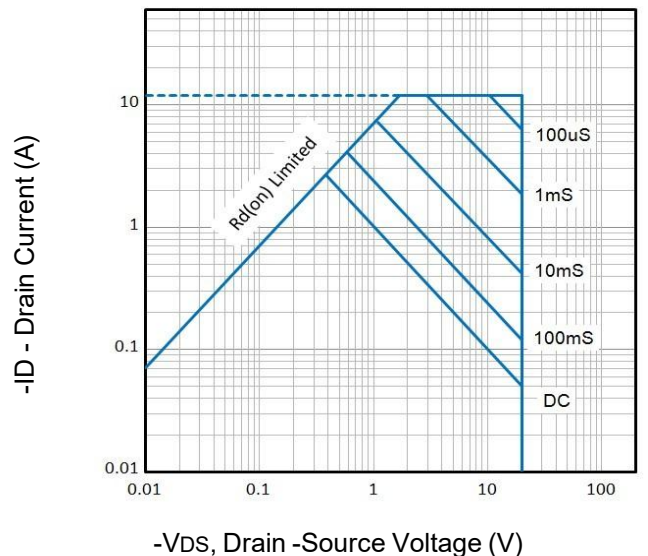


Fig6. Maximum Safe Operating Area

Typical Characteristics

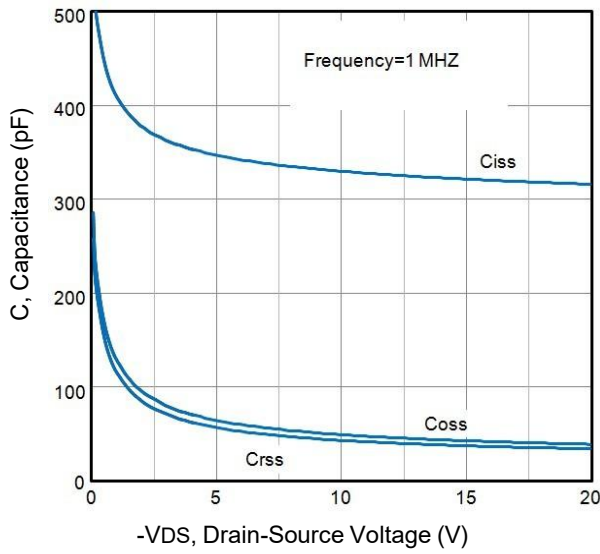


Fig7. Typical Capacitance Vs. Drain-Source Voltage

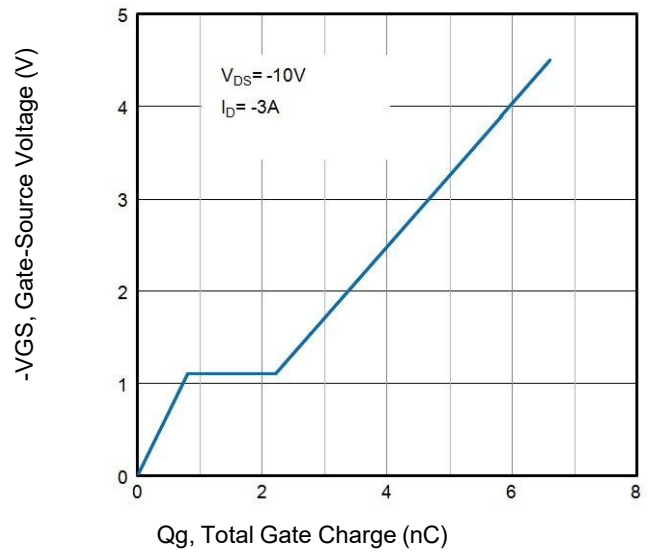


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

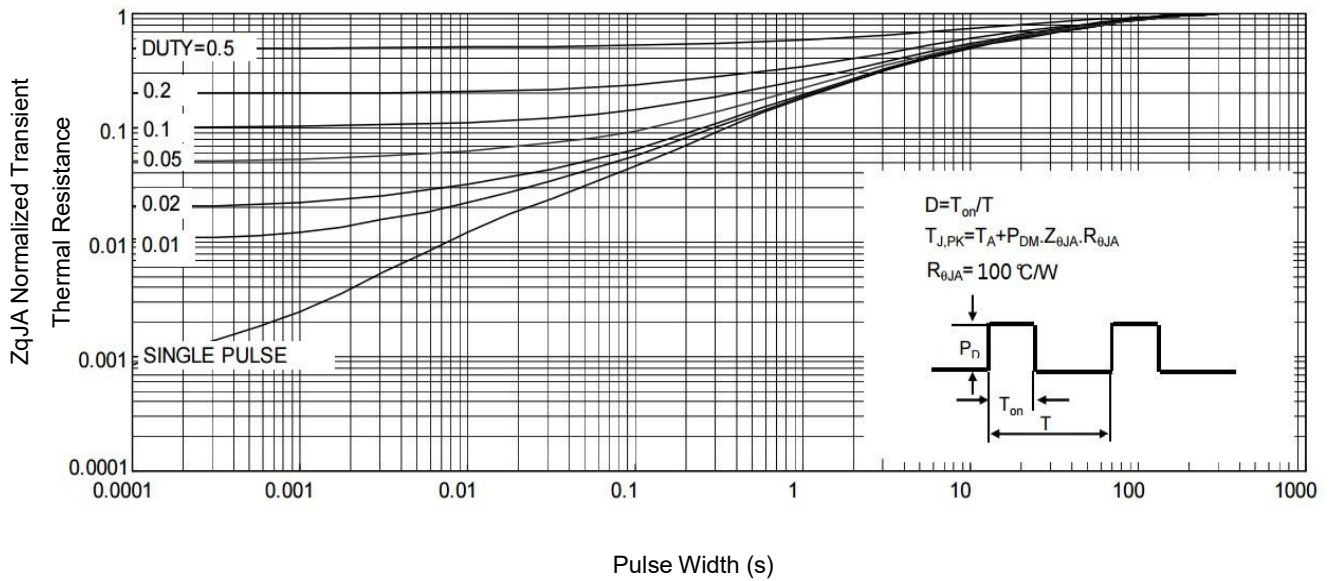


Fig9. Normalized Maximum Transient Thermal Impedance

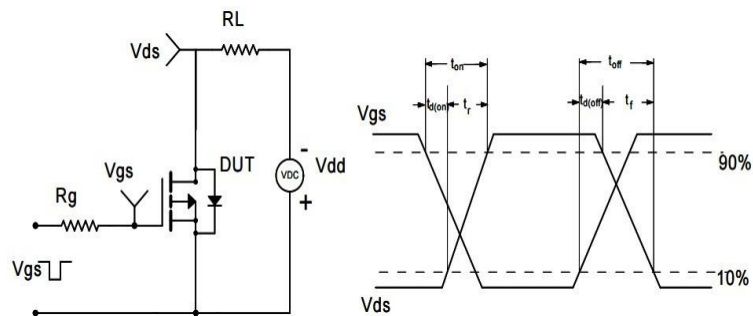
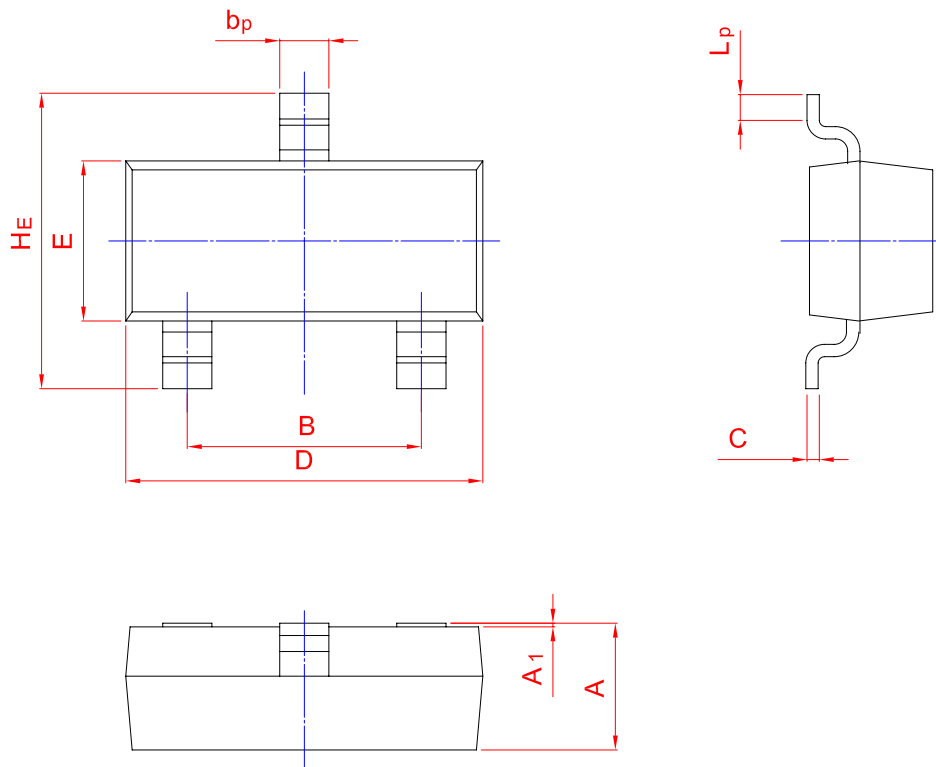
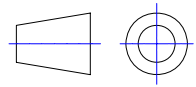


Fig10. Switching Time Test Circuit and waveforms

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



UNIT	A	B	b _p	C	D	E	H _E	A ₁	L _p
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20

SOT-23 Suggested Pad Layout

