

1-Line, Uni-directional, Transient Voltage Suppressors

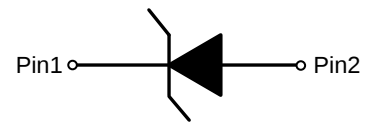
Descriptions

The ESD7D500TR is a Uni-directional TVS (Transient Voltage Suppressor). It is specifically designed to protect sensitive electronic components that may be subjected to ESD (Electrostatic Discharge), EFT (Electrical Fast Transients) and Lightning. It is particularly well-suited for cellular phones, portable device, digital cameras, power supplies and many other portable applications because of its small package and low weight.

The ESD7D500TR may be used to provide ESD protection up to $\pm 30\text{KV}$ Air, $\pm 30\text{KV}$ contact compliance to IEC61000-4-2, and withstand peak pulse current up to 30 A (8/20 μs) according to IEC61000-4-5.



DFN1006-2L



Circuit diagram

Features

- Stand-off voltage: $\pm 7\text{V}$ Max
- Transient protection for each line according to IEC61000-4-2 (ESD): $\pm 30\text{KV}$ Air, $\pm 30\text{KV}$ contact IEC61000-4-4(EFT): 40 A (5/50 ns) IEC61000-4-5 (Surge): 30 A (8/20 μs)
- Solid-state silicon technology
- Low leakage current

Applications

- Cell phone handsets and accessories
- Personal Digital Assistants (PDAs)
- Notebooks, Desktops, and Servers
- Portable Instrumentation
- Digital Cameras
- CAR/MID DVD/MP3/MP4/PMP Players

Order information

Device	Marking	Package	Shipping
ESD7D500TR	L1	DFN1006-2L	10000/Tape&Reel

Absolute maximum ratings

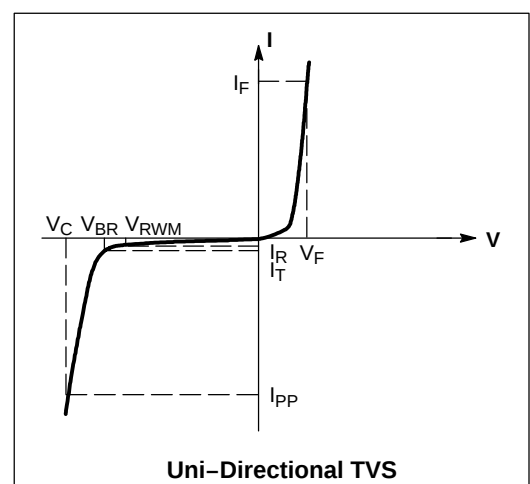
Parameter	Symbol	Rating	Unit
Peak pulse current ($t_p = 8/20\mu s$)	I_{PP}	30	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	± 30	kV
ESD according to IEC61000-4-2 contact discharge		± 30	
Operation junction temperature	T_J	-50~125	$^{\circ}C$
Lead temperature	T_L	260	$^{\circ}C$
Storage temperature	T_{STG}	-65~150	$^{\circ}C$

Electrical characteristics ($T_A=25^{\circ}C$, unless otherwise noted)

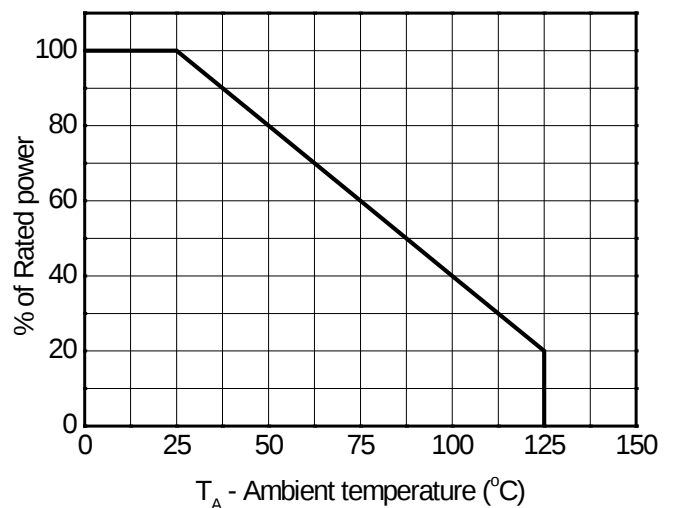
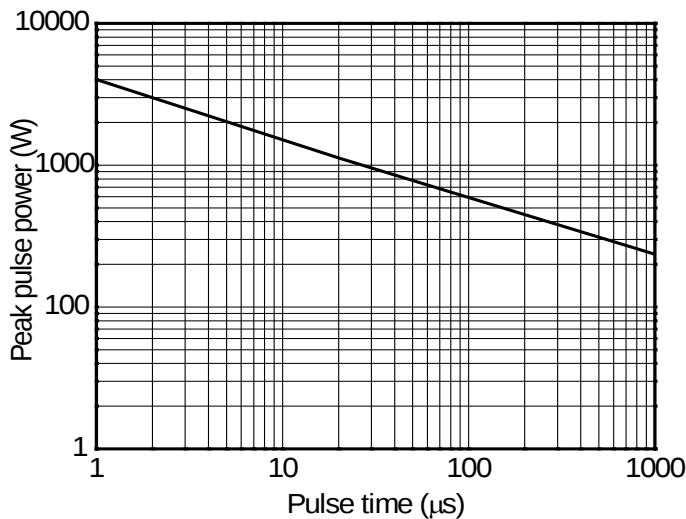
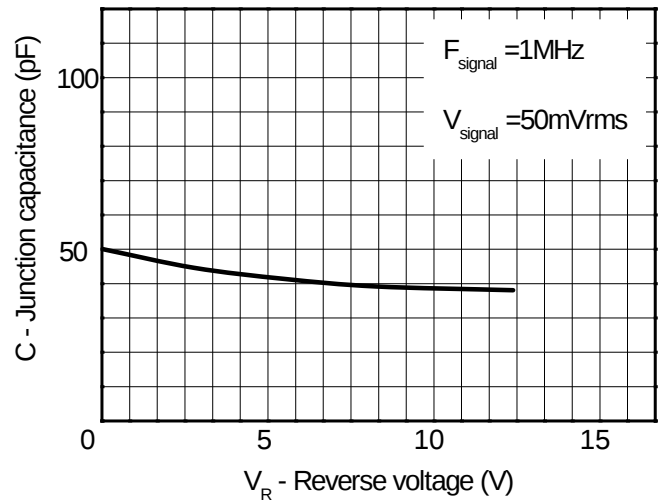
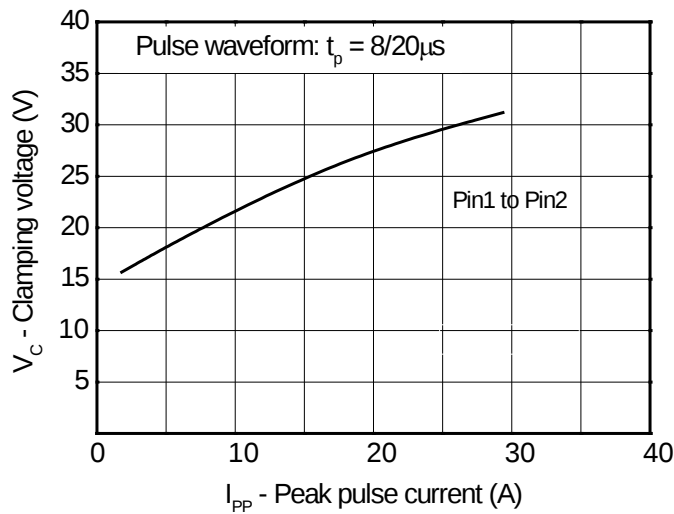
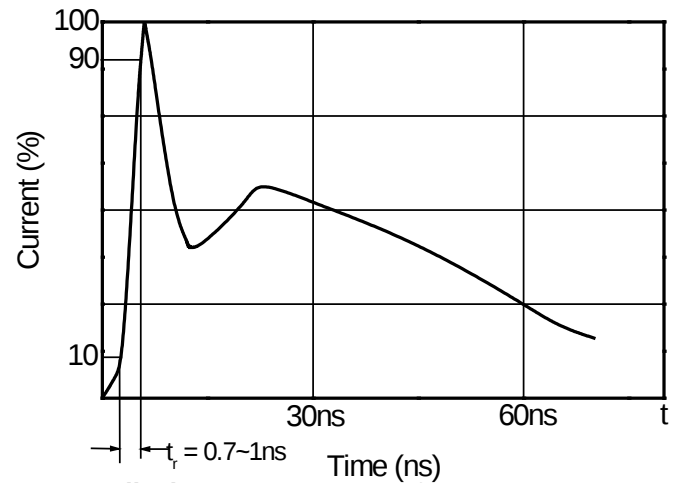
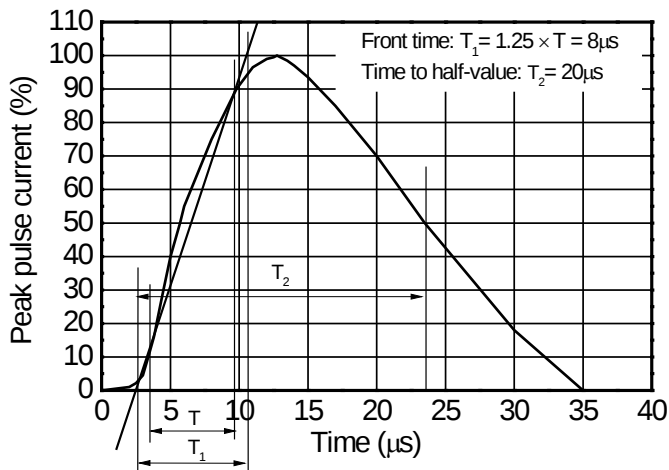
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				7	V
Reverse leakage current	I_R	$V_{RWM} = 7V$			0.5	μA
Reveres breakdown voltage	V_{BR12}	$I_T=1mA$	7.5			V
Clamping voltage	V_C	$I_{pp}=30 A$ $t_p=8/20\mu s$			32.0	V
Junction capacitance	C_J	$V_R = 0V$, $f = 1MHz$		50.0	60.0	pF

Electrical performance curve

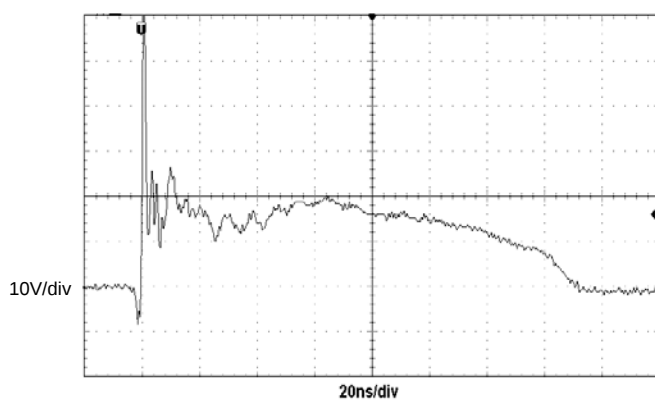
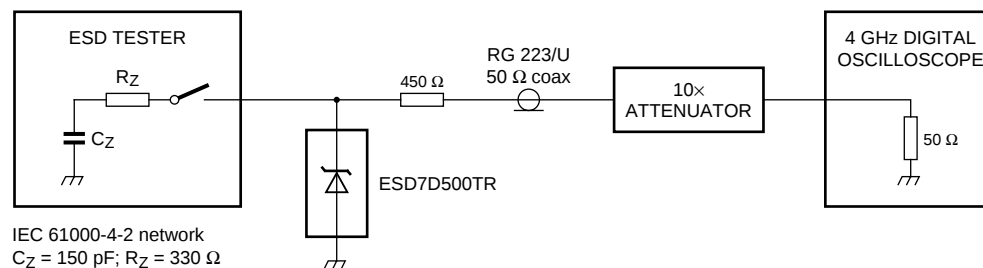
Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
I_F	Forward Current
V_F	Forward Voltage @ I_F



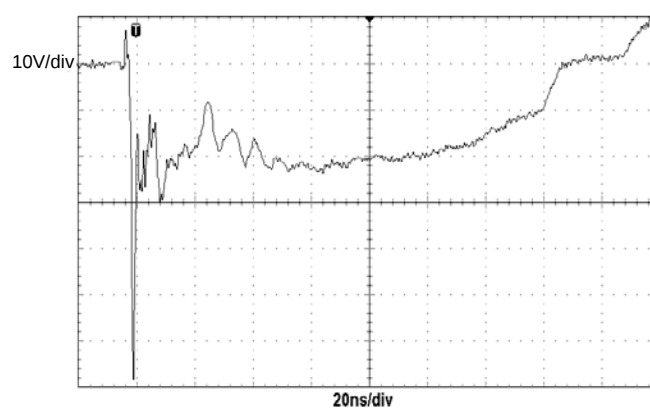
Typical characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)



ESD clamping test setup and waveforms



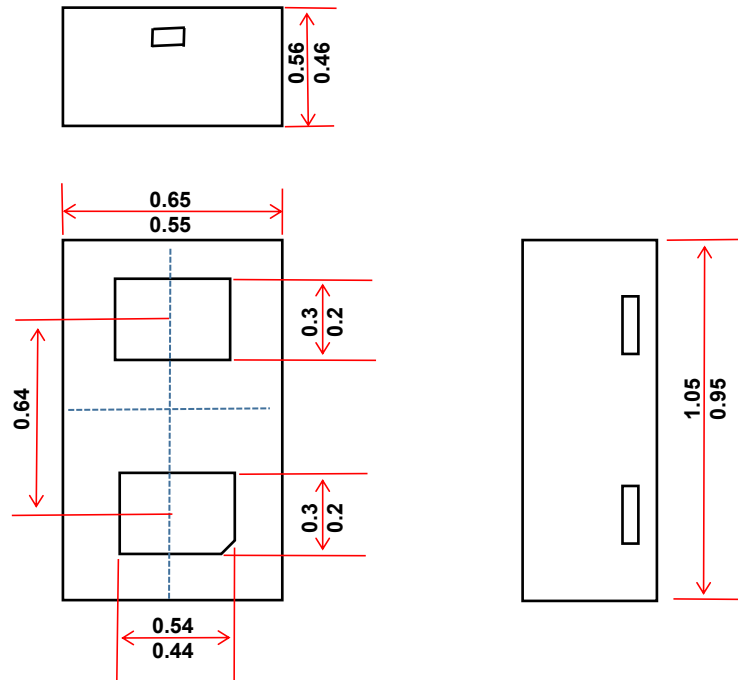
ESD clamping
(+8kV contact discharge per IEC61000-4-2)



ESD clamping
(-8kV contact discharge per IEC61000-4-2)

Package outline dimensions

SOD882



Recommended Mounting Pad Layout Unit:mm

